

## 60V N-Channel Enhancement Mode Power MOSFET

### Description

WML025N06HG2 uses Wayon's 2<sup>nd</sup> generation power trench MOSFET technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance. This device is well suited for high efficiency fast switching performance.

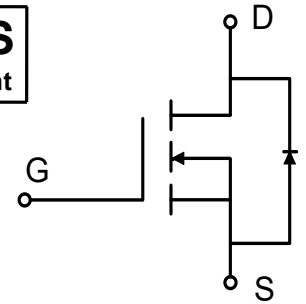


### Features

- $V_{DS} = 60V$ ,  $I_D = 180A$  (Silicon Limited)  
 $R_{DS(on)} < 3.2m\Omega @ V_{GS} = 10V$
- Low  $R_{DS(on)}$
- Low Gate Charge
- 100% EAS Guaranteed



**RoHS**  
compliant



### Applications

- DC/DC Converter
- Synchronous Rectification

### Absolute Maximum Ratings

Parameter		Symbol	Value	Unit
Drain-Source voltage		$V_{DS}$	60	V
Gate-Source voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1</sup>	$T_C = 25^\circ C$	$I_D$	180	A
	$T_C = 100^\circ C$		90	
Pulsed Drain Current <sup>2</sup>		$I_{DM}$	570	A
Single Pulse Avalanche Energy <sup>3</sup>		<b>EAS</b>	605	mJ
Avalanche Current		$I_{AS}$	55	A
Total Power Dissipation <sup>4</sup>	$T_C = 25^\circ C$	$P_D$	208	W
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55 to 150	$^\circ C$

### Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient <sup>1</sup>	$R_{\theta JA}$	45	$^\circ C/W$
Thermal Resistance from Junction-to-Case <sup>1</sup>	$R_{\theta JC}$	0.6	$^\circ C/W$

**Electrical Characteristics**  $T_c = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	60	-	-	V
Gate-Body Leakage current	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$T_J = 25^\circ\text{C}$ $V_{DS} = 60V, V_{GS} = 0V$	-	-	1	$\mu A$
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2	3	4	V
Drain-Source on-Resistance <sup>2</sup>	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 20A$	-	2.7	3.2	m $\Omega$
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 30V, V_{GS} = 0V,$ $f = 1\text{MHz}$	-	4683	-	pF
Output Capacitance	$C_{oss}$		-	1192	-	
Reverse Transfer Capacitance	$C_{rss}$		-	69	-	
<b>Switching Characteristics</b>						
Gate Resistance	$R_g$	$V_{DS} = 0V, V_{GS} = 0V,$ $f = 1\text{MHz}$	-	3	-	$\Omega$
Total Gate Charge	$Q_g$	$V_{GS} = 10V, V_{DS} = 50V,$ $I_D = 50A$	-	96	-	nC
Gate-Source Charge	$Q_{gs}$		-	19.5	-	
Gate-Drain Charge	$Q_{gd}$		-	12.1	-	
Turn-on Delay Time	$t_{d(on)}$	$V_{GS} = 10V, V_{DS} = 30V,$ $R_G = 2\Omega, I_D = 25A$	-	20.8	-	nS
Rise Time	$t_r$		-	5.2	-	
Turn-off Delay Time	$t_{d(off)}$		-	78.8	-	
Fall Time	$t_f$		-	24.9	-	
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage <sup>2</sup>	$V_{SD}$	$I_S = 1A, V_{GS} = 0V$	-	-	1.2	V
Continuous Source Current <sup>1,5</sup>	$I_S$	$V_G = V_D = 0V, \text{Force Current}$	-	-	180	A
Reverse Recovery Time	$t_{rr}$	$I_F = 25A, di_F/dt = 100A/\mu s$	-	67	-	nS
Reverse Recovery Charge	$Q_{rr}$		-	72	-	nC

## Notes:

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$
3. The EAS data shows Max. rating. The test condition is  $V_{DD} = 25V, V_{GS} = 10V, L = 0.4\text{mH}, I_{AS} = 55A$
4. The power dissipation is limited by 150°C junction temperature
5. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.

### Typical Characteristics

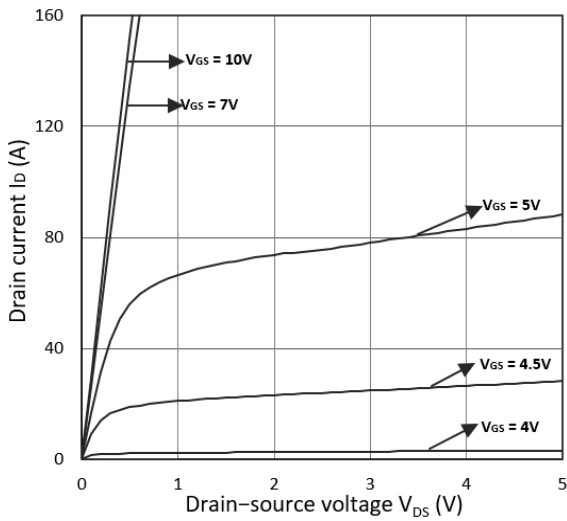


Figure 1. Output Characteristics

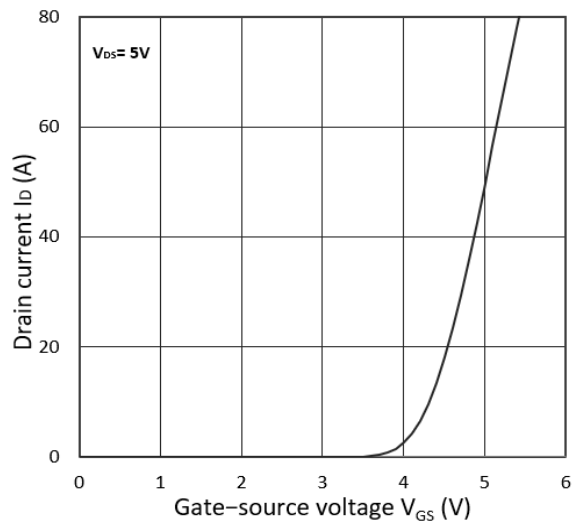


Figure 2. Transfer Characteristics

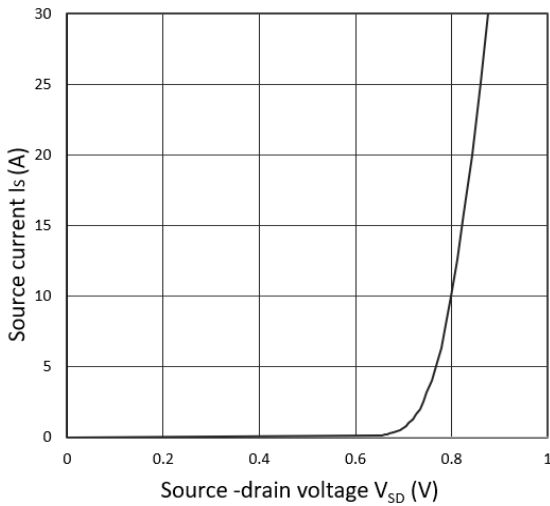


Figure 3. Forward Characteristics of Reverse

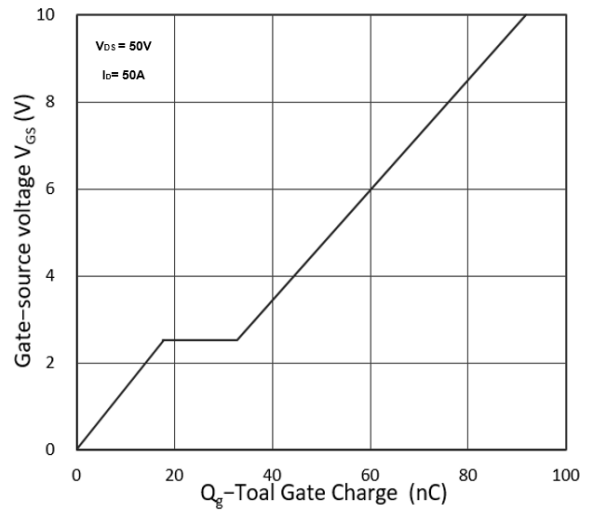


Figure 4. Gate Charge Characteristics

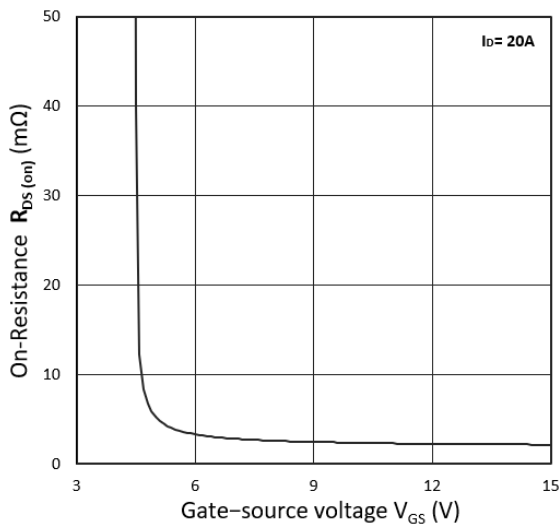


Figure 5.  $R_{DS(on)}$  vs.  $V_{GS}$

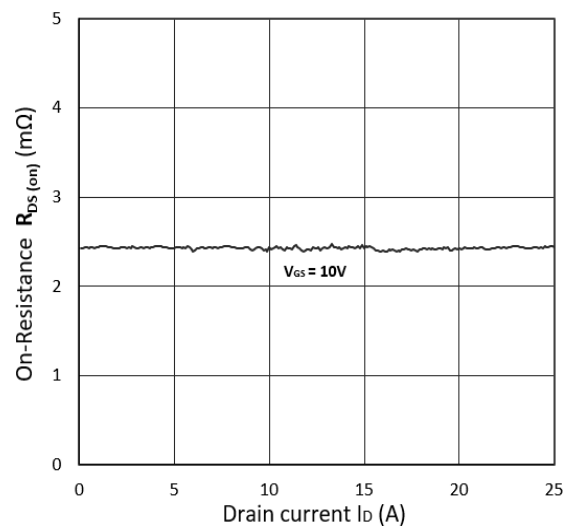


Figure 6.  $R_{DS(on)}$  vs.  $I_D$

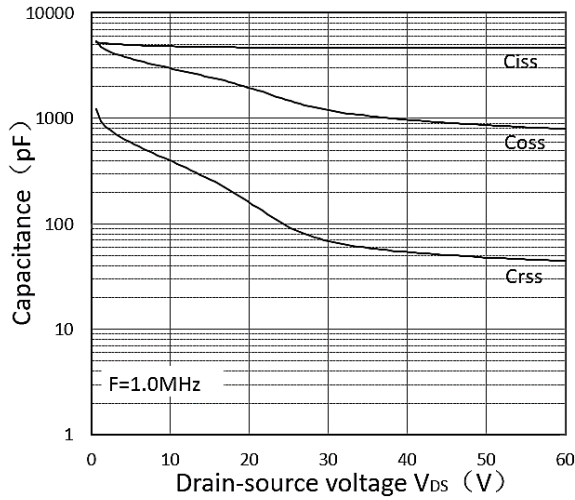


Figure 7. Capacitance Characteristics

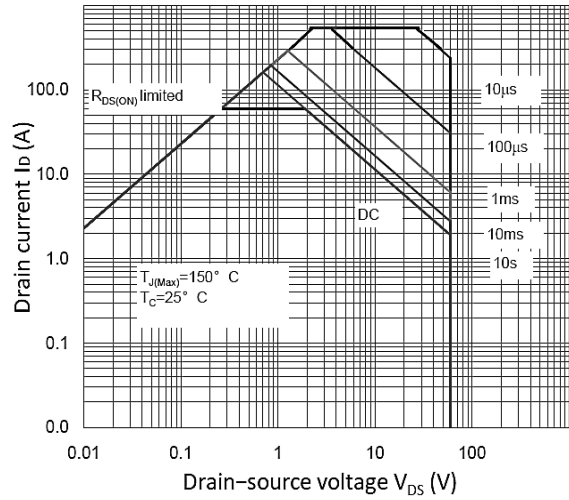


Figure 8. Safe Operating Area

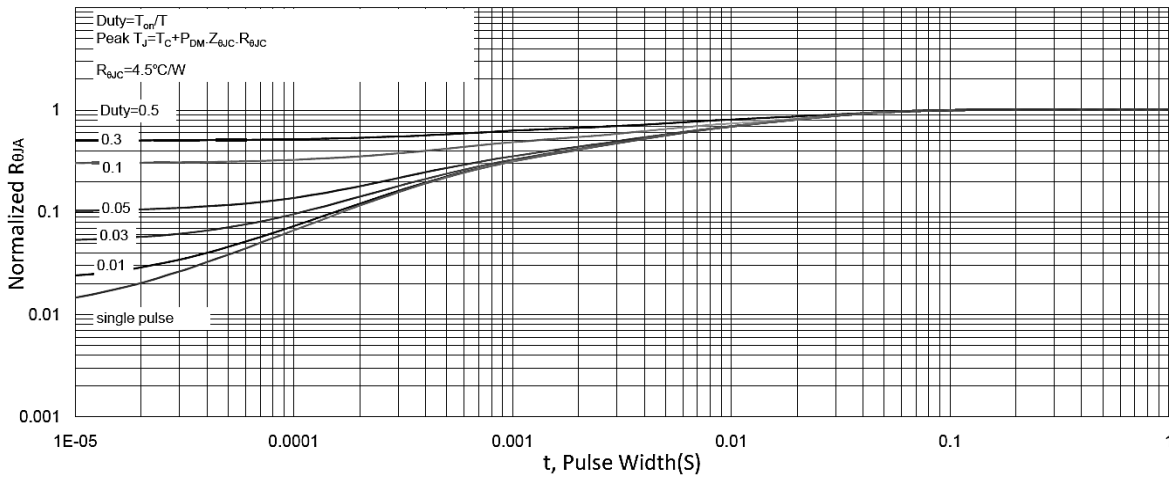


Figure 9. Normalized Maximum Transient Thermal Impedance

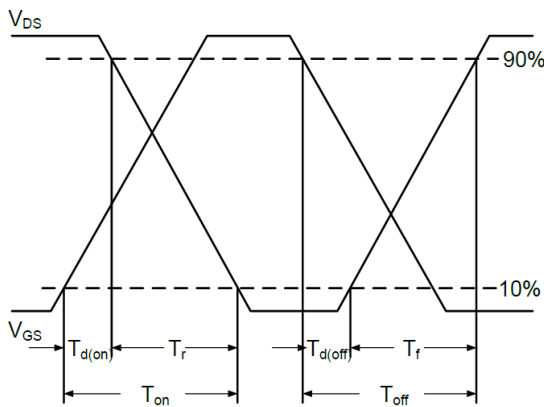


Figure 10. Switching Time Waveform

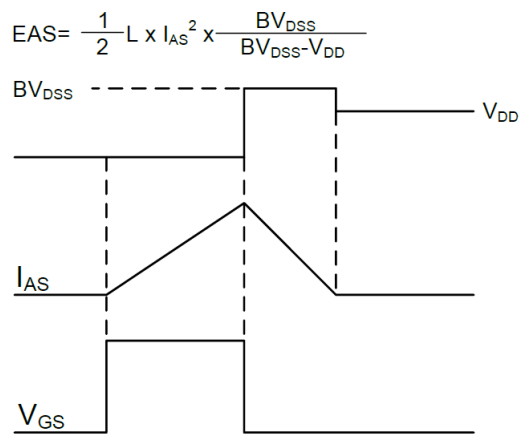
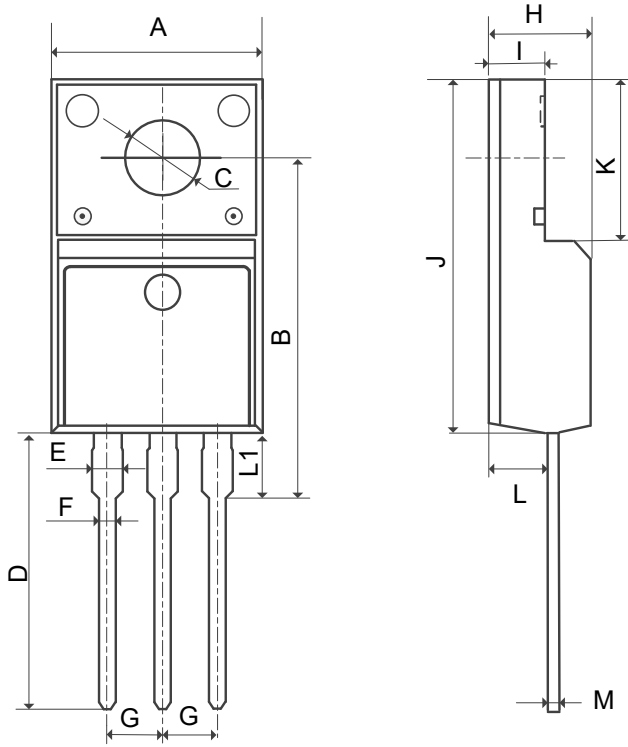


Figure 11. Unclamped Inductive Switching Waveform

## Mechanical Dimensions for TO-220F

## COMMON DIMENSIONS

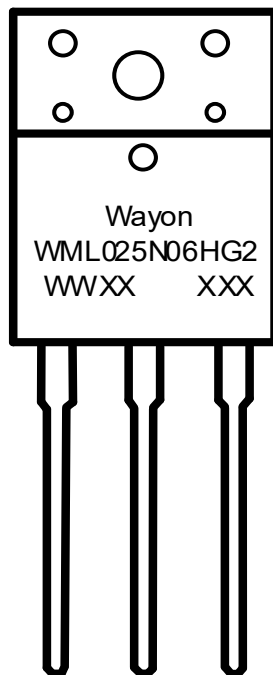


SYMBOL	MM	
	MIN	MAX
A	9.96	10.36
B	15.10	16.10
C	3.03	3.38
D	12.64	13.38
E	1.18	1.58
F	0.65	0.95
G	2.54REF	
H	4.50	4.90
I	2.34	2.74
J	15.57	16.17
K	6.70REF	
L	2.56	2.96
M	0.40	0.60
L1	2.85	3.50

## Ordering Information

Part	Package	Marking	Packing method
WML025N06HG2	TO-220F	WML025N06HG2	Tube

## Marking Information



WML025N06HG2 = Device code

WWXX XXX= Date code


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