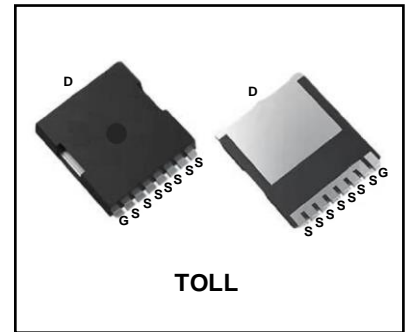


85V N-Channel Enhancement Mode Power MOSFET

Description

WMLL029NV8HGS uses Wayon's advanced power trench MOSFET technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance. This device is well suited for high efficiency fast switching applications.

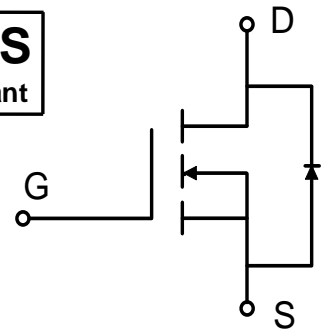


Features

- $V_{DS} = 85V$, $I_D = 240A$
 $R_{DS(on)} < 3m\Omega @ V_{GS} = 10V$
- High Speed Power Switching
- Low Gate Charge
- Low $R_{DS(on)}$
- 100% EAS Guaranteed



RoHS
compliant



Applications

- DC/DC Converter
- Power Management Switching
- Motor Driver

Absolute Maximum Ratings ($T_c = 25^\circ C$, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	85	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	$T_C = 25^\circ C$	240
		$T_C = 100^\circ C$	151.8
Pulsed Drain Current ⁴	I_{DM}	960	A
Single Pulse Avalanche Energy ³	EAS	720	mJ
Total Power Dissipation	P_D	312.5	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ¹	$R_{\theta JA}$	10	$^\circ C/W$
Thermal Resistance from Junction-to-Lead	$R_{\theta JC}$	0.4	$^\circ C/W$

Electrical Characteristics (Tc = 25°C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	85	-	-	V
Gate-body Leakage current	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	± 100	nA
Zero Gate Voltage Drain Current	$T_J=25^\circ C$	I_{DSS} $V_{DS} = 80V, V_{GS} = 0V$	-	-	1	μA
	$T_J=100^\circ C$		-	-	100	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2	3	4	V
Drain-Source on-Resistance ²	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 20A$	-	2.3	3	m Ω
Forward Transconductance ²	g_{fs}	$V_{DS} = 10V, I_D = 20A$	-	66	-	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS} = 40V, V_{GS} = 0V,$ $f = 1MHz$	-	8490	-	μF
Output Capacitance	C_{oss}		-	1400	-	
Reverse Transfer Capacitance	C_{rss}		-	83	-	
Switching Characteristics						
Gate Resistance	R_g	$V_{DS}=0V, V_{GS}=0V,$ $f=1MHz$	-	0.75	-	Ω
Total Gate Charge	Q_g	$V_{GS} = 10V, V_{DS} = 40V,$ $I_D = 20A$	-	143	-	nC
Gate-Source Charge	Q_{gs}		-	33	-	
Gate-Drain Charge	Q_{gd}		-	45.7	-	
Turn-on Delay Time	$t_{d(on)}$	$V_{GS} = 10V, V_{DS} = 40V,$ $R_G = 3\Omega, I_D = 20A$	-	38	-	ns
Rise Time	t_r		-	95	-	
Turn-off Delay Time	$t_{d(off)}$		-	75.5	-	
Fall Time	t_f		-	15	-	
Drain-Source Body Diode Characteristics						
Diode Forward Voltage ²	V_{SD}	$I_S = 20A, V_{GS} = 0V$	-	-	1.2	V
Continuous Source Current ^{1,5}	I_S	$V_G = V_D = 0V,$ Force Current	-	-	240	A
Reverse Recovery Time ²	t_{rr}	$I_F = 20A, di/dt = 100A/\mu s$	-	120	-	ns
Reverse Recovery Charge ²	Q_{rr}		-	260	-	nC

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The EAS data shows Max. rating . The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.4mH, I_{AS}=60A$
- Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^\circ C$.
- The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

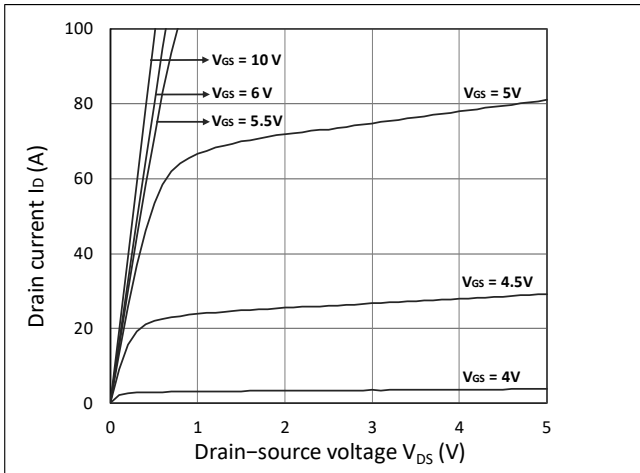


Figure 1. Output Characteristics

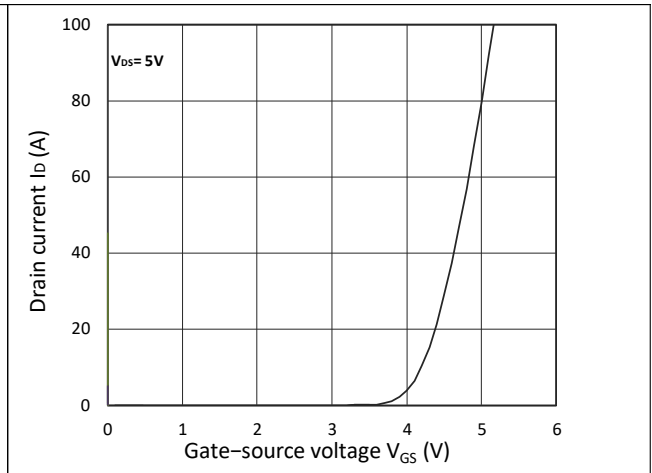


Figure 2. Transfer Characteristics

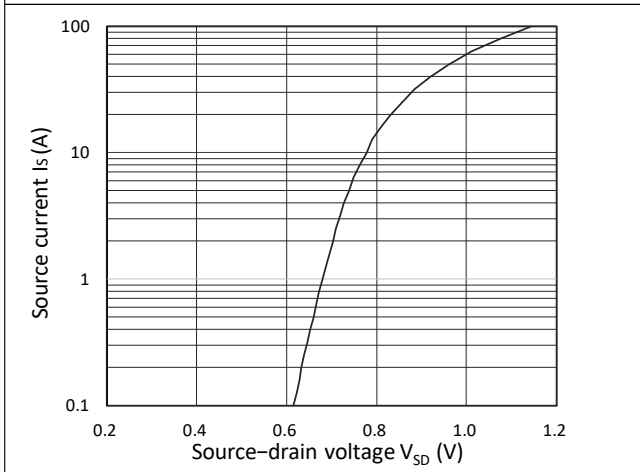


Figure 3. Forward Characteristics of Reverse

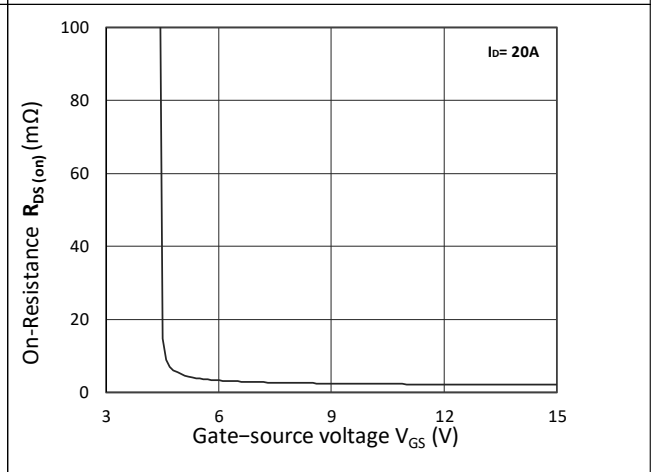


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

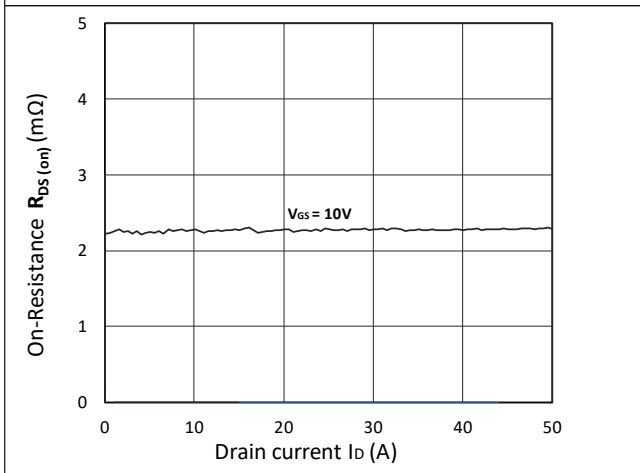


Figure 5. $R_{DS(ON)}$ vs. I_D

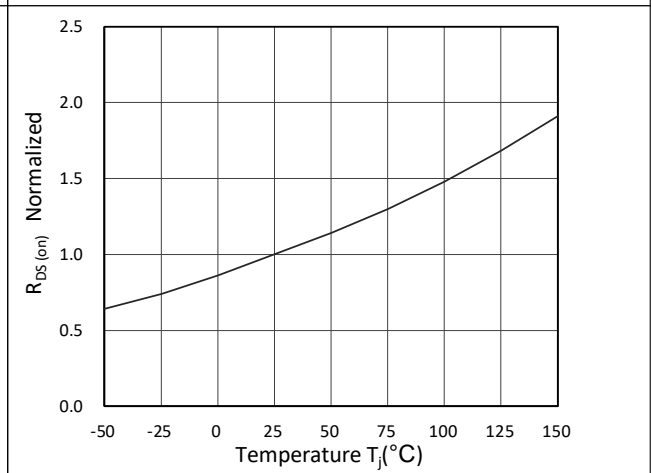


Figure 6. Normalized $R_{DS(ON)}$ vs. Temperature

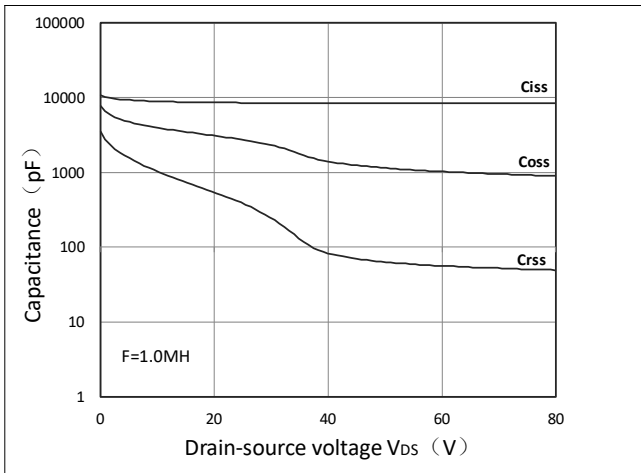


Figure 7. Capacitance Characteristics

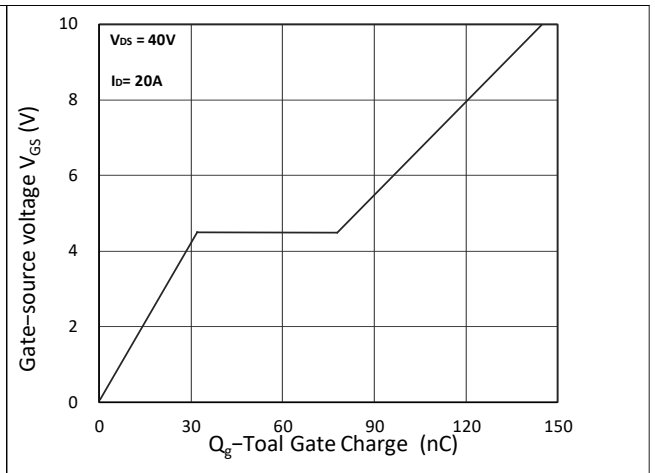


Figure 8. Gate Charge Characteristics

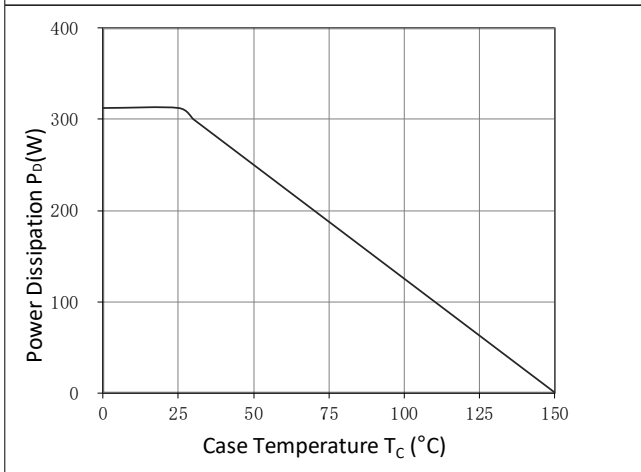


Figure 9. Power Dissipation

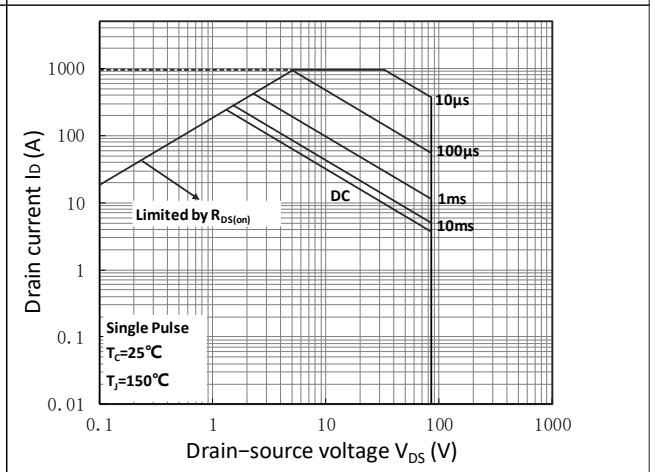


Figure 10. Safe Operating Area

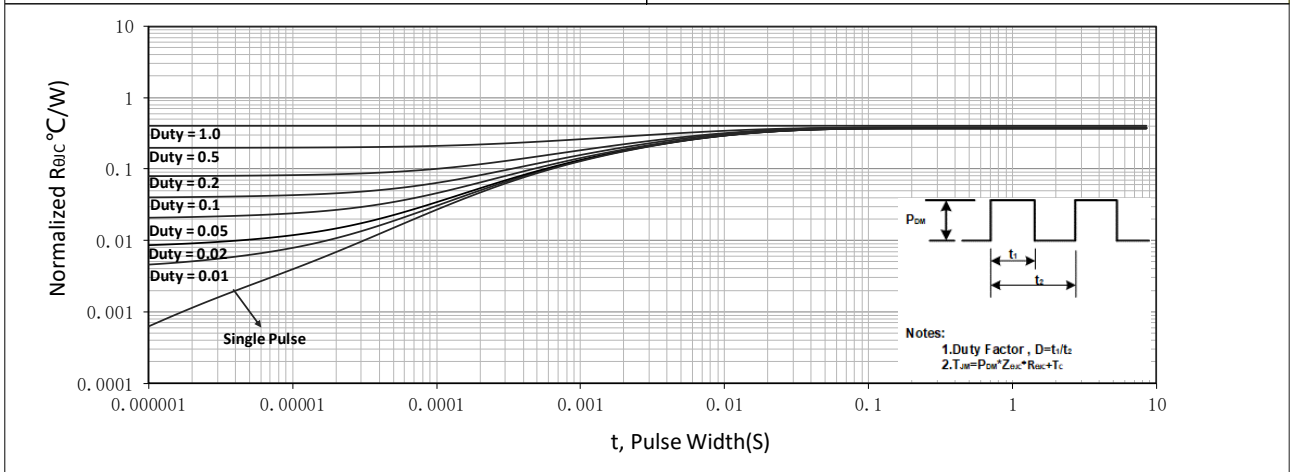


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

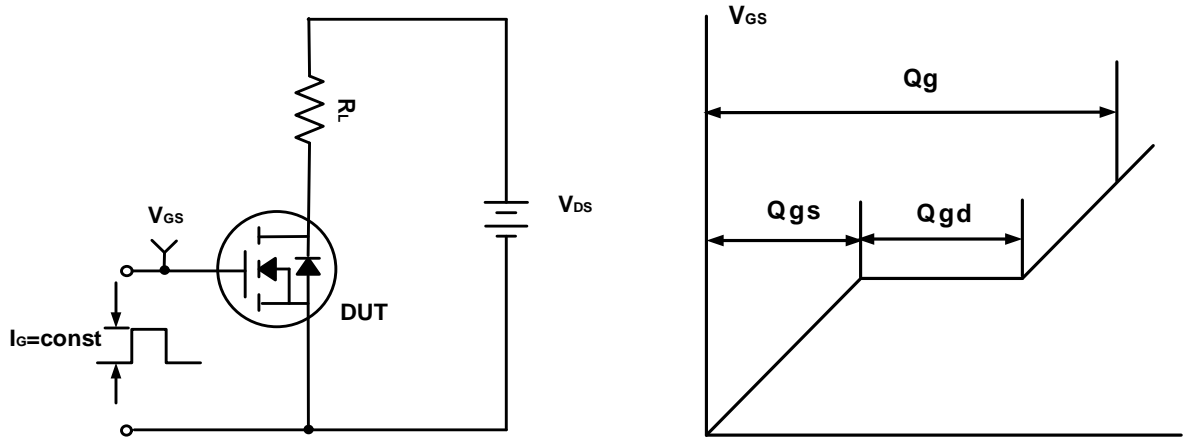


Figure A. Gate Charge Test Circuit & Waveforms

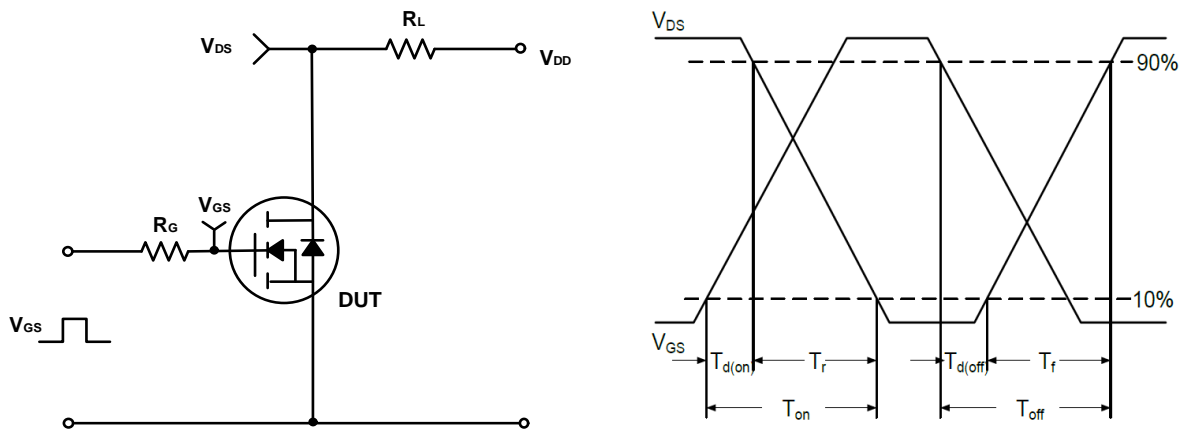


Figure B. Switching Test Circuit & Waveforms

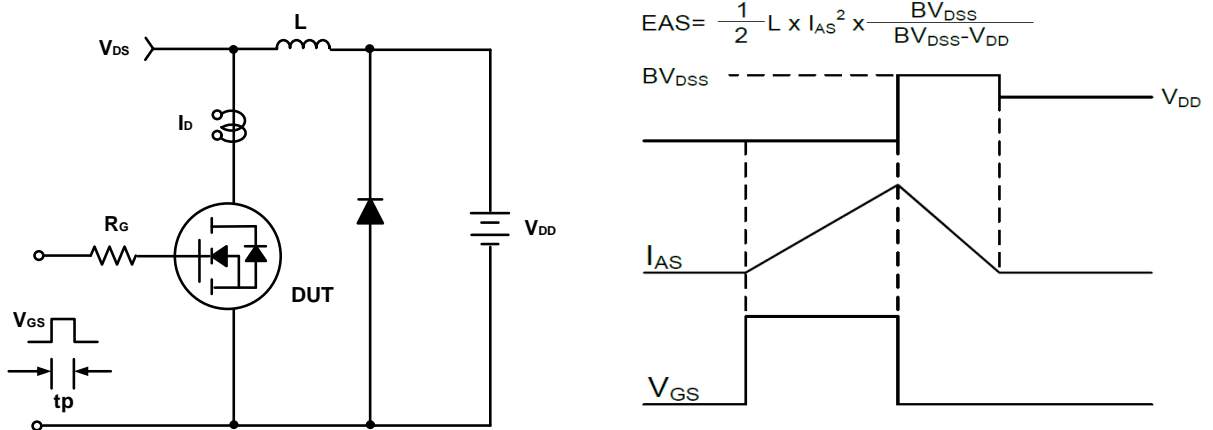
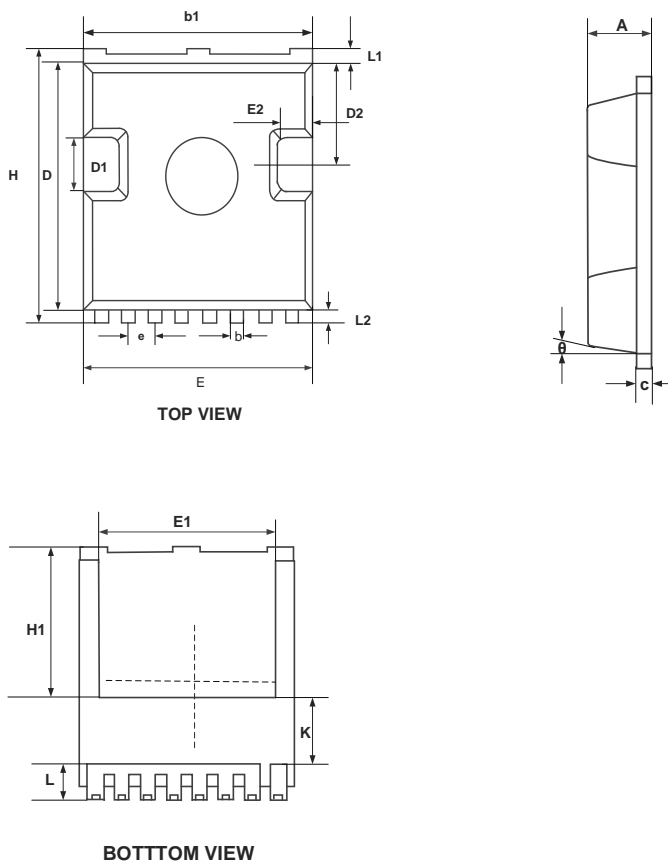


Figure C. Unclamped Inductive Switching Circuit & Waveforms

Mechanical Dimensions for TOLL



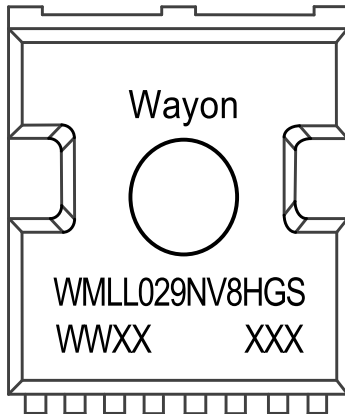
COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	2.20	2.40
b	0.60	0.85
b1	9.70	9.90
c	0.40	0.60
D	10.30	10.50
D1	3.20	3.40
D2	4.47	4.67
E	9.80	10.00
E1	8.00	8.20
E2	0.50	0.70
e	1.200 BSC	
H	11.60	11.80
H1	6.95 BSC	
K	3.10 REF	
L	1.55	1.75
L1	0.60	0.80
L2	0.50	0.70
θ	10° REF	

Ordering Information

Part	Package	Marking	Packing method
WMLL029NV8HGS	TOLL	WMLL029NV8HGS	Tape and Reel

Marking Information



WMLL029NV8HGS = Device code

WWXX XXX= Date code

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WAYON website: <http://www.way-on.com>

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